

# All-in-One Power Solution for Automotive Infotainment—Single IC Produces Five Rails Directly from Battery

Jin-Jyh Su and Terry Groom  
*Analog Devices, Inc.*

Our connected and media driven lifestyles are a result of, or a reason for, technology's push into every aspect of our lives, including today's highly integrated automotive infotainment systems. The complex mix of electronic components contained in automobile infotainment systems mirrors consumer electronics: high performance microcontrollers, memory, interface, and driver ICs. The power supply picture is just as complex, as each component may require a variety of low voltage rails with wide ranging power requirements. Complexity is not limited to the infotainment systems. Automotive performance, fuel efficiency, and driver convenience features require increasingly advanced electronic systems. The power system also stands between sensitive electronics and the unwelcoming conditions of the automotive setting—namely the wide-ranging voltage and predictably transient battery environment serving as input. A well-designed power system must both power and protect electronics, even as manufacturers make the automotive environment less inviting to electronics with features such as start-stop technology.

Start-stop technology magnifies the extreme conditions that electronics must face, specifically through repeated engine cranking. A start-stop enabled car restarts the engine repeatedly, but critical systems must remain operational even as the battery supply goes through a cold crank each time—while not catastrophic, a driver suddenly singing acapella as the car's music goes silent may not add to the car's positive reviews.

At the other end of the spectrum, ultralow quiescent current is a critical requirement of automobile power systems. An automobile may need to sit unused for a month or more as critical always-on electronic systems quietly run, without draining the battery.

The ADI Power by Linear™ **LTC3372** all-in-one high voltage controller is capable of maintaining regulation through the extreme voltage changes presented by automotive battery environments. It can keep always-on components running without draining the battery because of its ultralow quiescent current. The LTC3372 features four configurable monolithic regulators and can provide up to five output channels for infotainment or other electronic systems.

## Automotive Multichannel Power

The LTC3372 significantly reduces the number of components required to produce multiple rails. It combines proven high voltage automotive controller technology with four configurable monolithic bucks to create a space- and cost-efficient automotive multichannel power solution.

The high voltage buck controller input operates through input surges up to 60 V, such as those seen during a load dump, and can also regulate through input dips as low as 4.5 V in a standard buck configuration, and down to 3 V in a SEPIC configuration. This operating input range enables uninterrupted power to sensitive electronics in the face of significant transients. The LTC3372's four low voltage bucks are configured independently by combining power stages from a selection of eight 1 A power stages. Stages are combined to meet the power requirements of each regulator, with eight possible unique 4-output channel configurations, all directly from an automotive battery source.

One benefit of a single IC multichannel power solution is shared internal voltage references and bias supplies. This bias sharing enables lower per channel  $I_Q$  specifications for multichannel power than would be available with independent ICs. For a single-channel, always-on supply, the  $V_{IN}$  referenced bias  $I_Q$  is 23  $\mu$ A typical and 46  $\mu$ A maximum at 150°C. With all 5 channels regulating in Burst Mode® operation, the typical bias current is only 60  $\mu$ A total, or 12  $\mu$ A per channel. The LTC3372 enables new always-on applications when its total bias  $I_Q$  for 5 channels is comparable to a single channel using older technology.

## Single-Chip Controller and Regulators

The LTC3372 is a front-end 60 V high voltage (HV) buck controller plus four low voltage (LV) 5 V monolithic buck regulators with low  $I_Q$  Burst Mode operation. By integrating a controller and monolithic regulators, LTC3372 can provide up to five separate rails from high input voltage in compact size at low cost. The output voltage of the HV controller can be selected between 3.3 V and 5 V depending on the level of  $V_{OUTPRG}$  pin; the

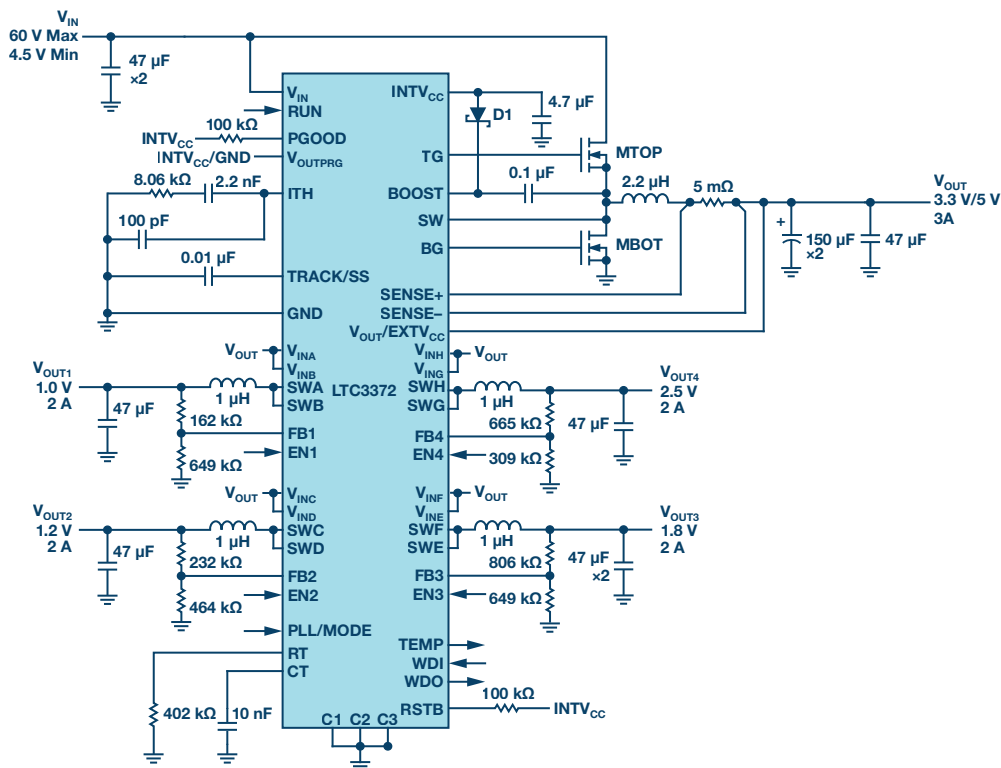


Figure 1. Typical 60 V input application of LTC3372. The HV controller feeds quad 2 A, 1 V/1.2 V/1.8 V/2.5 V LV regulators. The 3.3 V/5 V HV controller output can be used as an additional 3 A current rail.

output voltages of the LV regulators can be individually programmed with external resistors through FB1 to FB4 pins.

Figure 1 and Figure 2 show a typical application and the corresponding efficiency of the HV controller. While the HV controller is typically used for feeding the LV regulators, each regulator operates independently via per channel enable and input pins. Additional flexibility is provided by the eight power stages. The eight switches can be distributed among the LV regulators, with the combination digitally configured through the C bit (C1, C2, C3) to meet rail-specific maximum current limits. Table 1 shows C bit settings and the resulting maximum current limit configurations per regulator number. Figure 3 shows how efficiency varies with the number of switches combined in parallel.

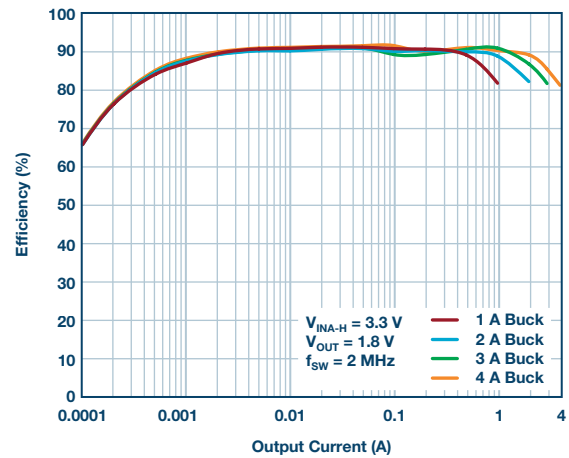


Figure 3. Burst Mode operation efficiency vs. output current of LV regulators. A 1 A, 2 A, 3 A, and 4 A buck represents different configurations when one, two, three, and four switches are connected in parallel.

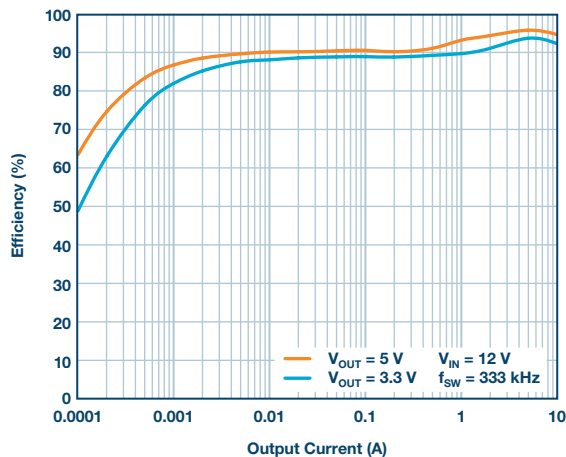


Figure 2. Burst Mode operation efficiency vs. output current of HV controller in Figure 1. The output current is shown up to 10 A, which is sufficient to feed four fully-loaded LV regulators and a 3 A, 3.3 V/5 V load.

Table 1. LV Regulator Configurations Are Set via C1, C2, and C3 Codes; In Any Configuration of Less Than Four LV Regulators, the Unused Regulators Enable Pins and Feedback Pins Are Connected to Ground

C3	C2	C1	BUCK 1	BUCK 2	BUCK 3	BUCK 4
0	0	0	2 A	2 A	2 A	2 A
0	0	1	3 A	1 A	2 A	2 A
0	1	0	3 A	1 A	1 A	3 A
0	1	1	4 A	1 A	1 A	2 A
1	0	0	3 A	2 A		3 A
1	0	1	4 A		2 A	2 A
1	1	0	4 A		1 A	3 A
1	1	1	4 A			4 A

The LTC3372 also provides an on-chip temperature sensor and watchdog timer features. The temperature sensor allows users to closely monitor die temperature whenever a LV regulator is enabled. The watchdog timer issues a reset signal if the microprocessor fails to clear the timer when malfunction occurs.

## Power Loss Optimization

Often, dc-to-dc converters are judged on their efficiency and are therefore designed to maximize that parameter, but optimizing dc-to-dc converters in terms of power loss (instead of just efficiency) usually pays higher performance dividends in high power applications. For a multistage converter system, such as can be created with the LTC3372, efficiency measurements can be misleading when some part of the efficiency is the composite of HV controller and LV regulators.

Keep in mind that power loss optimization does not mean simply minimizing total power loss, but also balancing the distribution of the losses across devices. A good approach is to begin with the LV regulators, as the total power loss across all LV regulators accounts for most of the loss in the LTC3372 system. By considering all applicable LV regulator configurations, a designer can compare a significant range of power loss options. Table 2 lists all applicable configurations and the associated power loss in a 1.2 V, 1.8 V, 2.5 V application at 3 A, 3 A, 0.5 A maximum load, respectively. The difference between the best and the worst configurations is 0.432 W. In normal cases, recursively allocating the largest possible switch to the highest power channel yields the best result.

**Table 2. Total Burst Mode Operation Power Loss in LV Regulators of 1.2 V (3 A), 1.8 V (3 A), 2.5 V (0.5 A) in Various Configurations;  $V_{IN-H}$  are 3.3 V and Switching Frequency is 2 MHz; The Best Configuration Yields 0.432 W Less Power Loss Compared to the Worst Case**

C3	C2	C1	BUCK 1	BUCK 2	BUCK 3	BUCK 4	Loss (W)
0	1	0	1.2 V (3 A)	2.5 V (0.5 A)	Off	1.8 V (3 A)	2.523
1	0	0	1.2 V (3 A)	2.5 V (0.5 A)		1.8 V (3 A)	2.486
1	1	0	1.2 V (3 A)		2.5 V (0.5 A)	1.8 V (3 A)	2.204
1	1	0	1.8 V (3 A)		2.5 V (0.5 A)	1.2 V (3 A)	2.181

More generic efficiency optimization procedures can be applied to the HV controller. The slight difference is that all/part of the HV controller's loading becomes the input referred current of LV regulators. When LV regulators are the only loads, the HV controller sees a moderate load, even if each LV regulator is fully loaded. Instead of selecting the low  $R_{DS}$  FETs or pursuing the highest peak efficiency, designers should focus on the operating current range of interest. The efficiency vs. output current curves of three FETs with distinct  $R_{DS}$  are shown in Figure 4. For the LV regulators in Table 2, using the highest  $R_{DS}$  but lowest  $Q_g$  FET yields the highest efficiency in the range below maximum load (3.759 A in the optimum configuration).

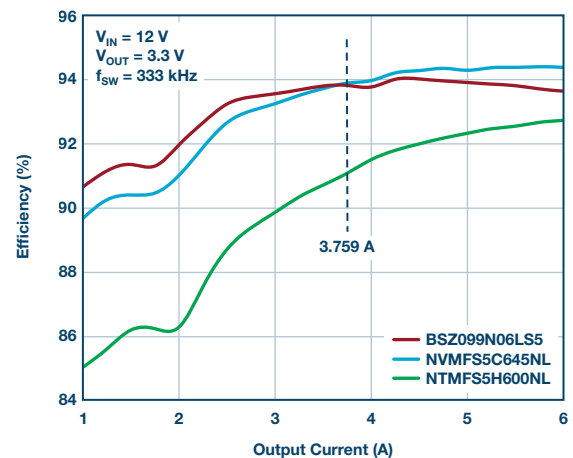


Figure 4. Burst Mode operation efficiency vs. output current with three different FETs in the HV controller. The same FET for both top and bottom are used. The figure is zoomed into 1 A to 6 A range to closely observe any crossover to determine the optimum FET for LV regulators in Table 2. 3.759 A is the maximum load current when LV regulators are fully loaded. The results suggest that the best selection is the highest  $R_{DS}$  but lowest  $Q_g$  FET (BSO99N06LS5).

## SEPIC Controller

In automotive applications, cold crank remains a challenge to dc-to-dc converters. Buck converters are forced to operate in dropout if the regulated output voltage is higher than the input in cold cranking situations. Two alternative front-end topologies, boost and SEPIC, can be realized using available resources provided in the LTC3372's HV controller to avoid dropout operation.

Even though the boost is slightly simpler, it passes any high voltage input surge to the following buck stage. This precludes the use of high efficiency low voltage buck regulators as the second step-down stage. In Figure 5, we configure the LTC3372 HV controller in a nonsynchronous SEPIC topology. The SEPIC converter generates a 5 V intermediate rail to power two 3.3 V/4 A LV regulators and to sustain the continuous operation of HV controller.

When two 4 A LV regulators are fully loaded, more than 5 A of current is drawn from the SEPIC output. The peak current through the sense resistor can easily exceed 10 A as the switch current is the summation of both inductor windings. Considering that the sense resistor is within the hot loop, some effort is required to produce a clean waveform at the current comparator inputs. One solution is to incorporate a differential filtering scheme as shown in the SEPIC schematic, and use a low inductance resistor fabricated in a reversed package.

Figure 6 shows SEPIC efficiency in Burst Mode operation and Figure 7 shows the SEPIC output voltage when a 12 V-to-3 V transient is applied to the input. Designers should not overlook the heat generated in the catch diode during PCB design. Thermal limits can be met by reserving extra space for a moderately oversized diode and using thicker copper. Another diode and a filtering capacitor are connected to the  $V_{IN}$  pin to avoid reversed current and sudden voltage spike due to input transients.

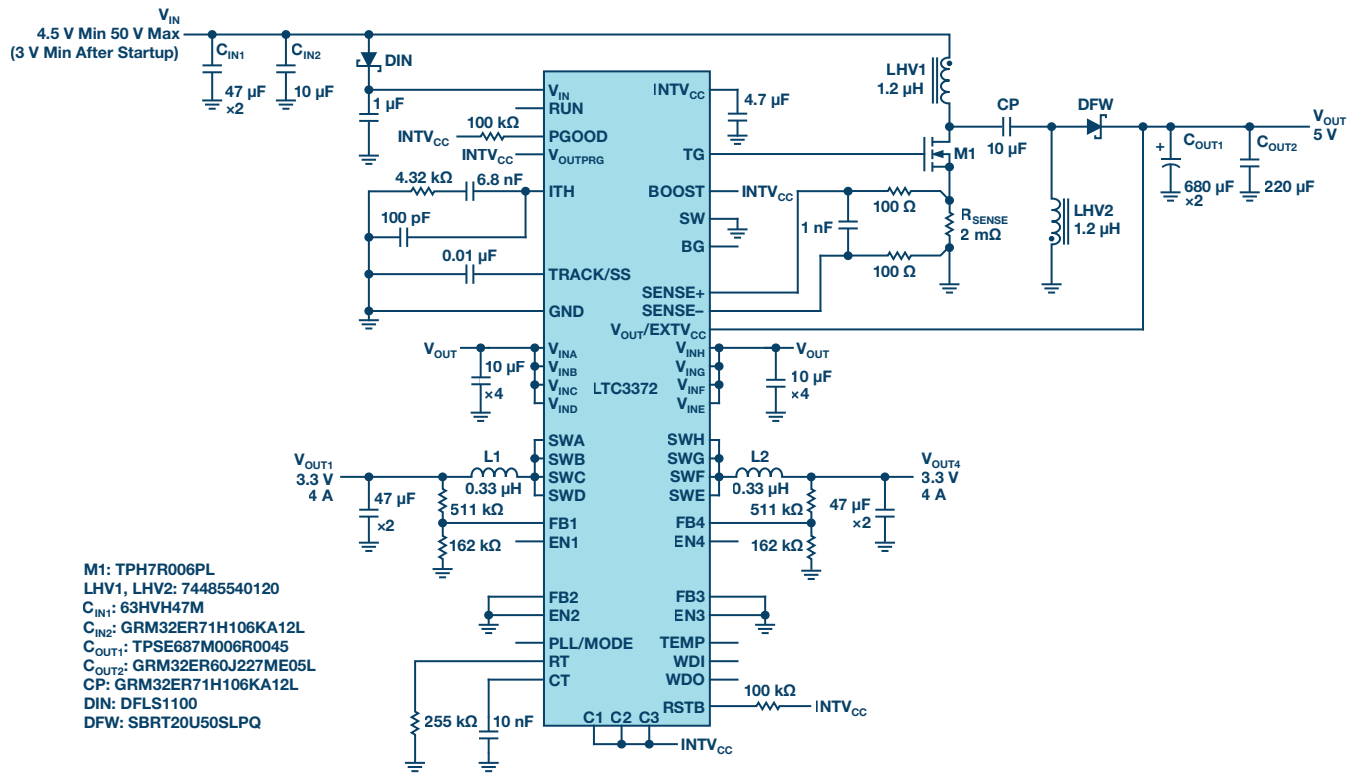


Figure 5. A 4.5 V to 50 V input nonsynchronous HV SEPIC converter feeding two 3.3 V/4 A LV regulators. After startup, the SEPIC converter can maintain 5 V at  $V_{OUT}$  with 3 V minimum  $V_{IN}$  when two LV regulators are fully loaded. Minimum  $V_{IN}$  down to 1.5 V is possible if a lighter load is placed on the SEPIC. The output of the SEPIC must be set to 5 V to sustain continuous operation when  $V_{IN}$  is below 5 V.  $D_{IN}$  and a 1  $\mu$ F capacitor are connected to the IC  $V_{IN}$  to prevent reverse current and transient spike. A differential current sense scheme and low inductance sense resistor are recommended to provides a clean signal at the current comparator inputs. Low inductance (LHV1 and LHV2), maximum switching frequency, and low bandwidth are compromises between right-half-plane zero and current ripple.

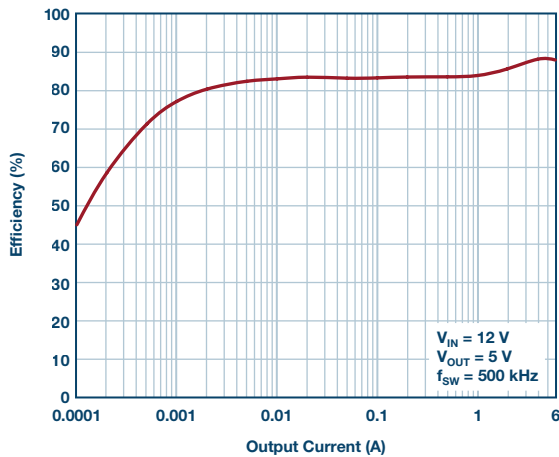


Figure 6. Burst Mode operation efficiency vs. output current of nonsynchronous SEPIC controller in Figure 5. The output current is shown up to 6 A, which is sufficient to feed two fully-loaded 3.3 V/4 A LV regulators.

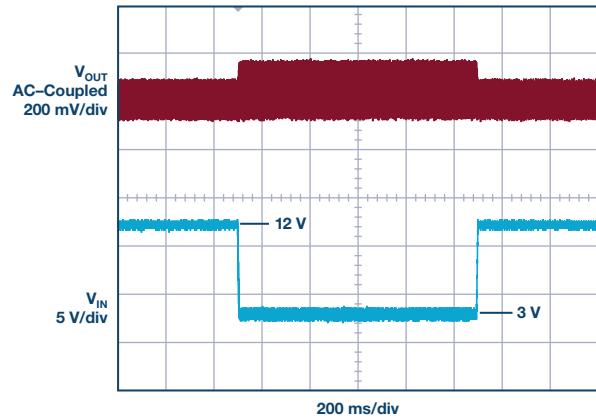


Figure 7. Output response of the SEPIC to an input transient similar to a cold crank condition. The input steps down from 12 V to 3 V within 2 ms and stays at 3 V for one second before recovering to 12 V. Larger ripple is observed during the 3 V transient, which is caused by the higher peak current flowing to the output capacitor through the catch diode. The waveforms are taken with two fully loaded 3.3 V/4 A LV regulators and 500 kHz SEPIC switching frequency.

### Conclusion

The LTC3372 provides a single chip solution for high voltage multichannel buck converters. Its low  $I_o$  operation and low cost per channel are a good fit for always-on systems in automotive applications.

## About the Authors

Jin-Jyh Su joined Linear Technology (now part of Analog Devices) as an analog IC design engineer after graduating from the Georgia Institute of Technology. He was with the Linear Analog Device Power Group in Milpitas, California and Dallas, Texas for 3 years. Su specializes in high performance dc-to-dc converter design. His design experience includes monolithic and controller products operating at high voltage (100 V), high speed (3 MHz), and low  $I_o$  for automotive applications. Su recently joined the Battery Management System (BMS) Group in Dallas, Texas where he is working on multicell battery stack monitors. He can be reached at [jin.su@analog.com](mailto:jin.su@analog.com).

Terry Groom is a power design section leader and has been with Analog Devices for almost 13 years. He is a graduate of Texas A&M and Georgia Institute of Technology. Groom's design group, located in Dallas, Texas, specializes in high performance dc-to-dc power controllers. He and his team have developed products with voltage inputs from 5 V to 150 V in multiple switch mode topologies with switching frequencies of up to 3 MHz. Groom has more than 30 years of direct design experience in a variety of analog disciplines. He is proud to be a valued member of the Analog Devices' Power by Linear development team. He can be reached at [terry.groom@analog.com](mailto:terry.groom@analog.com).

## Online Support Community



Engage with the Analog Devices technology experts in our online support community. Ask your tough design questions, browse FAQs, or join a conversation.

Visit [ez.analog.com](http://ez.analog.com)

### Analog Devices, Inc. Worldwide Headquarters

Analog Devices, Inc.  
One Technology Way  
P.O. Box 9106  
Norwood, MA 02062-9106  
U.S.A.  
Tel: 781.329.4700  
(800.262.5643, U.S.A. only)  
Fax: 781.461.3113

### Analog Devices, Inc. Europe Headquarters

Analog Devices GmbH  
Ott-Aicher-Str. 60-64  
80807 München  
Germany  
Tel: 49.89.76903.0  
Fax: 49.89.76903.157

### Analog Devices, Inc. Japan Headquarters

Analog Devices, KK  
New Pier Takeshiba  
South Tower Building  
1-16-1 Kaigan, Minato-ku,  
Tokyo, 105-6891  
Japan  
Tel: 813.5402.8200  
Fax: 813.5402.1064

### Analog Devices, Inc. Asia Pacific Headquarters

Analog Devices  
5F, Sandhill Plaza  
2290 Zuchongzhi Road  
Zhangjiang Hi-Tech Park  
Pudong New District  
Shanghai, China 201203  
Tel: 86.21.2320.8000  
Fax: 86.21.2320.8222

©2019 Analog Devices, Inc. All rights reserved. Trademarks and registered trademarks are the property of their respective owners. Ahead of What's Possible is a trademark of Analog Devices. DN21147-0-2/19

[analog.com](http://analog.com)



AHEAD OF WHAT'S POSSIBLE™